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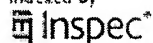
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 Suyama, K.; Fang, S.-C.; Tsvividis, Y.P.;
Solid-State Circuits, IEEE Journal of
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 Yu-Cherng Hung; Bin-Da Liu;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
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 Volume 50, Issue 5, May 2003 Page(s):673 - 677
 Digital Object Identifier 10.1109/TCSI.2003.811025
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 Elwakil, A.S.; Ozoguz, S.;
Electronics Letters
 Volume 39, Issue 11, 29 May 2003 Page(s):831 - 833
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- ☐ 4. **Multi-step A/D converters with time-multiplexed comparators**
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 Thain, W.E., Jr.; Connelly, J.A.;
Circuits and Devices Magazine, IEEE
 Volume 8, Issue 4, July 1992 Page(s):8 - 16
 Digital Object Identifier 10.1109/101.146298
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6. **High-speed low-power sense comparator for multilevel flash memories**
Pierin, A.; Gregori, S.; Khouri, O.; Micheloni, R.; Torelli, G.;
[Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International](#)
[on](#)
Volume 2, 17-20 Dec. 2000 Page(s):759 - 762 vol.2
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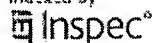
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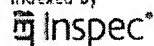
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